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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

GROUP: 2818

Yuuichi HIRANO, et al.

SERIAL NO: 10/813,038

ATTENTION:

FILING RECEIPT CORRECTIONS

FILED: March 31, 2004

FOR: SEMICONDUCTOR STORAGE DEVICE HAVING HIGH SOFT-ERROR
IMMUNITY

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

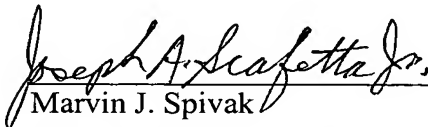
Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
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Sir:

The Patent Office is requested to provide a corrected Official Filing Receipt for the
attached. No fees are required. If you have any questions, please do not hesitate to contact us.

Respectfully Submitted,

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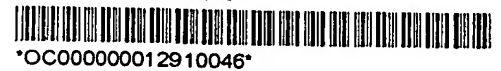
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/813,038	03/31/2004	2818	770	251076US2 DIV	17	11	1

22850
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CONFIRMATION NO. 3827
FILING RECEIPT



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Date Mailed: 06/14/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

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Assignment For Published Patent Application

Renesas Technology Corp., Tokyo, JAPAN; *

Domestic Priority data as claimed by applicant

This application is a DIV of 10/201,921 07/25/2002 PAT 6,756,692

Foreign Applications

JAPAN 2001-243292 08/10/2001

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Early Publication Request: No

RECEIVED: 6/16/04
OBLON, SPIVAK, MCCLELLAND
MAIER & NEUSTADT, P.C.
DOCKETING DEPT.
Initials/Date Docketed: KR 6/17/04
Type of Resp(s):
Due Date(s):

Title

Semiconductor storage device having soft-error immunity

Preliminary Class

365

PLEASE NOTE THAT THE TITLE IS INCORRECT. IT SHOULD READ AS FOLLOWS:

SEMICONDUCTOR STORAGE DEVICE HAVING HIGH SOFT-ERROR IMMUNITY